

## CLAIMS

1. A high-temperature treatment process, comprising the steps of:  
supporting first sides of a plurality of silicon substrates on a support fixture having  
support surfaces consisting essentially of silicon, wherein second sides of said  
5 substrates opposite said first sides are substantially monocrystalline;  
placing said support fixture supporting said silicon substrates into an oven;  
flowing an ambient gas comprising hydrogen into said oven; and  
treating said silicon substrates in said ambient gas at a temperature of at least 1100°C.

2. The process of claim 1, wherein said support fixture comprises a plurality of legs  
10 extending along respective axes and composed of virgin polysilicon and including teeth  
projecting at inclined angles from stem portions of said legs, said support surfaces being formed  
at ends of said teeth to extend perpendicularly to said axes.

3. The process of claim 2, wherein said inclined angles are in a range of 87° to 89½°  
with respect to said axes.

4. The process of claim 2, wherein there are four of said legs and said support surfaces  
15 are disposed in a square pattern to support said substrates.

5. The process of claim 4, wherein said support surfaces are disposed in said square  
pattern at locations located at approximately 0.707 of four radii of said substrate.

6. The process of claim 2, wherein there are three of said legs and said support surfaces  
20 are disposed in an equilateral triangle to support said substrates at locations located at  
approximately 0.707 of three radii of said substrate.

7. The process of claim 1, wherein said second sides of said substrates are polished.

8. The process of claim 1, wherein said substrates are substantially circular silicon wafers, further comprising the prior steps of:

cutting said wafers from one or more silicon ingots; and

polishing at least said second sides of said wafers.

9. A high-temperature wafer processing method, comprising the steps of:

providing a tower comprising

a plurality of silicon legs, extending along respective axes, and each having a plurality

of teeth cut therein at inclined angles with respect to said axes and having

support areas formed on ends of said teeth to extend perpendicularly to said

axes, and

two bases fixed to opposed ends of said plurality of legs;

supporting a plurality of substrates on said support areas; and

processing said substrates at a processing temperature of at least 1100°C.

10. The method of claim 9, wherein said plurality of legs consist of four legs and said support surfaces are disposed in a square pattern about a wafer center.

11. The method of claim 9, wherein said plurality of legs comprise three legs and said support surfaces are disposed at 0.707 of three wafer radii in an equilateral triangle pattern about a wafer center.

12. The method of claim 9, wherein said processing includes exposing said wafers to a treatment ambient comprising hydrogen at said processing temperature.

13. The method of claim 9, wherein said processing temperature is at least 1250°C and is maintained for an extended predetermined length of time.

14. The method of claim 9, wherein said legs comprise virgin polysilicon.

15. The method of claim 9, wherein said inclined angles are in a range of between  $87^{\circ}$  and  $89\frac{1}{2}^{\circ}$ .

16. A wafer supporting tower, comprising:

5 two bases comprising silicon;

a plurality of legs joined at opposite ends thereof to said two bases and comprising  
silicon; and

a plurality of slots cut into said legs to form teeth extending from stem portions of said  
legs at angles of between  $87^{\circ}$  and  $89\frac{1}{2}^{\circ}$  and having support surfaces formed on  
10 ends thereof for supporting wafers thereon.

17. The tower of claim 16, wherein said legs comprise polysilicon.

18. The tower of claim 17, wherein said polysilicon is virgin polysilicon.

19. The tower of claim 18, wherein said virgin polysilicon is chlorine-free virgin  
polysilicon.

15 20. The tower of claim 16, wherein said legs consist essentially of silicon.